

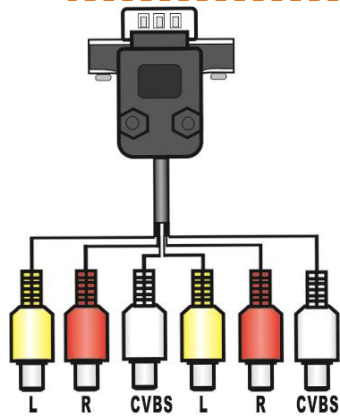
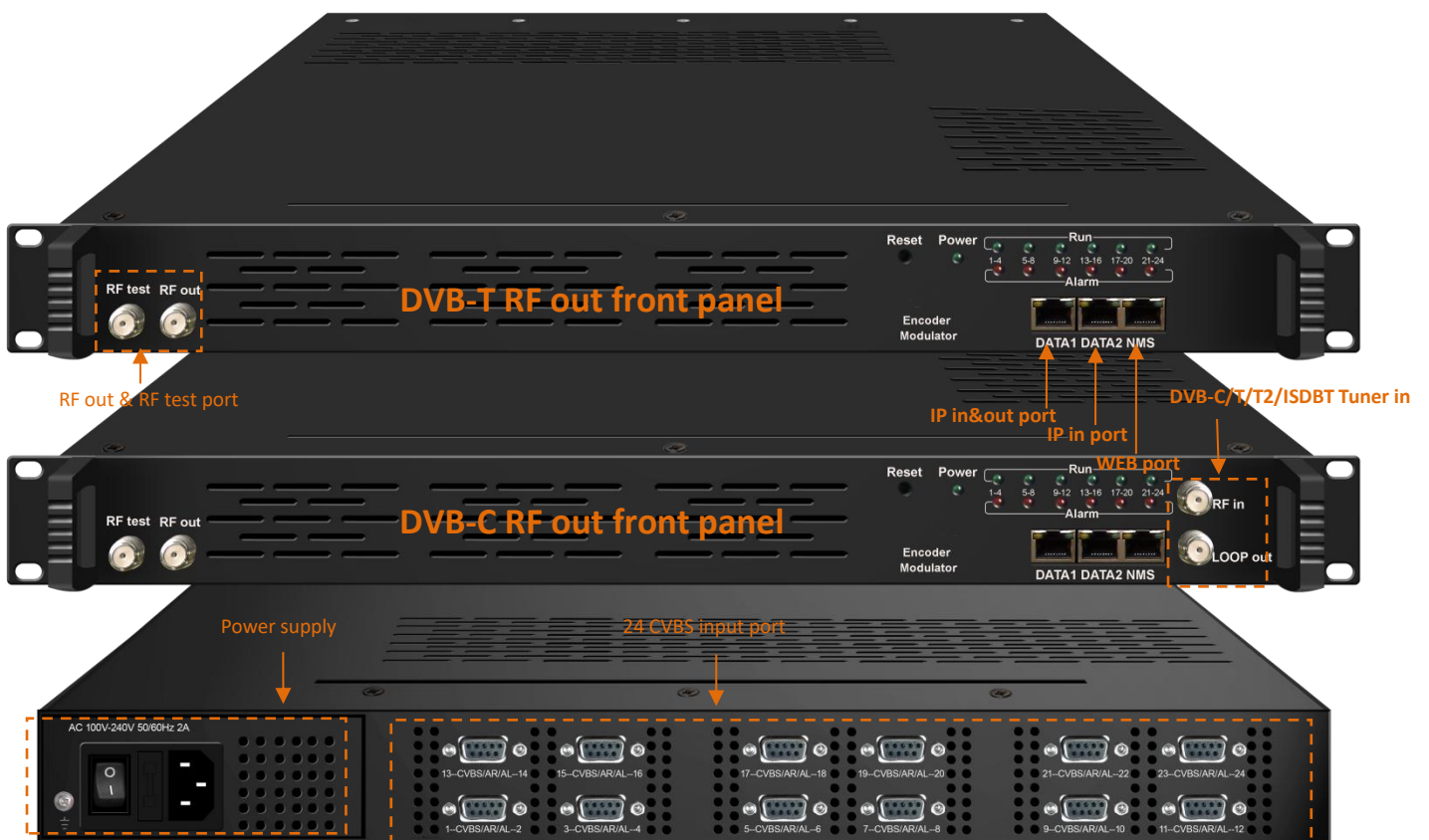


HPS824IV

Encoder Modulator

8/12/16/20/24 CVBS → 4 DVB-C/4 DVB-T

MPEG2 AC3 encoding



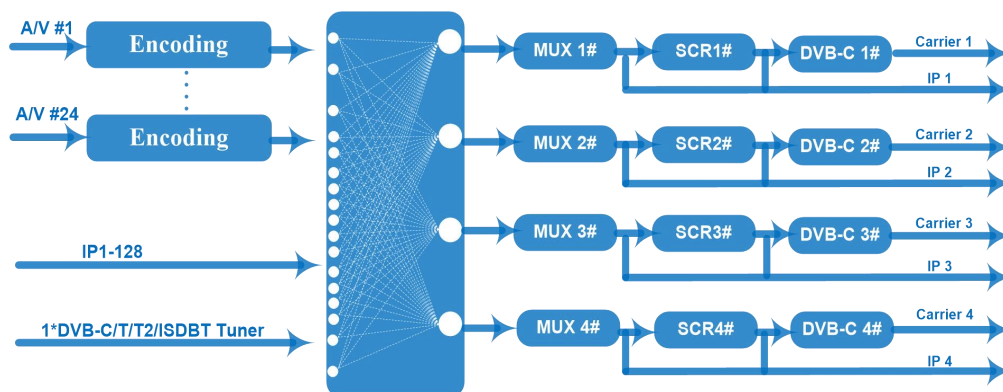
Product Overview

HPS824IV encoder modulator is a professional high integration device which includes encoding, multiplexing, scrambling and modulation. It supports 8/12/16/20/24 CVBS inputs, 128 IP input through Data1 (GE) and Data2 (FE) port. It also supports DVB-C/T RF out with 4 adjacent carries, and supports 4 MPTS out thru Data1 (GE). This full function device makes it ideal for small CATV head end system, and it's a smart choice for hotel TV system, entertainment system in sports bar, hospital, apartment...

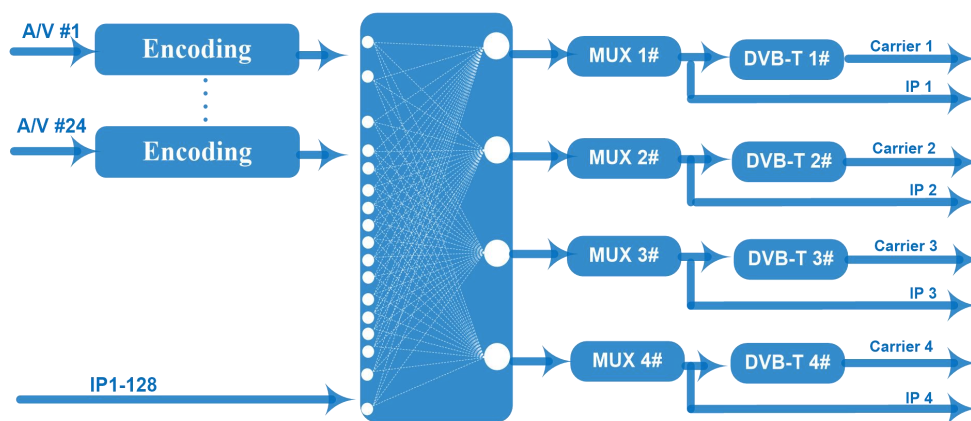
Key features

- 8/12/16/20/24 A/V inputs with **MPEG2 Encoding**
- MPEG1 Layer II, **AC3(2.0) Audio encoding** and support audio gain adjustment
- 1 DVB-C/T/T2/ISDBT tuner input for re-mux (For DVB-C RF out version. Suitable for India market.) (Optional)
- 128 IP input over UDP and RTP protocol
- 4 groups of multiplexing/scrambling/modulation output channels
- Support PID remapping/ accurate PCR adjusting/PSI/SI editing and inserting
- Support 4 MPTS IP (DATA1 port only) output over UDP and RTP
- Support 4 DVB-C or 4 DVB-T RF out
- Support CC (closed caption)
- **Support LOGO, Caption, QR code insertion---Language Supported: 中文, English, اردو, for more languages please consult us...) optional**
- Control via web management, and easy updates via web
- **Lowest cost per channel, breakthrough price**

Principle Chart



DVB-C RF output



DVB-T RF output

Technical specification

Input	8/12/16/20/24 CVBS inputs , RCA interface , 1 DVB-C/T/T2/ISDBT Tuner for remux, F type interface (only for DVB-C RF out) 128 IP input over UDP and RTP, DATA1 and DATA2,RJ45		
Tuner Section	DVB-C	Standard	J.83A(DVB-C), J.83B, J.83C
		Frequency In	60 MHz~890 MHz
		Constellation	16/32/64/128/256 QAM
	DVB-T/T2	Frequency In	60 MHz~890 MHz
		Bandwidth	6/7/8 M bandwidth
ISDB-T	Frequency In	60 MHz~890 MHz	
Video	Resolution	720×480_60i, 544×480_60i, 352×480_60i 352×240_60i,320×240_60i,176×240_60i, 176×120_60i	
		720×576_50i,704×576_50i,640×576_50i, 352×288_50i 320×288_50i, 176×288_50i, 176×144_50i	
	Encoding	MPEG-2	
	Bit-rate	0.5Mbps~8Mbps each channel	
	Rate Control	CBR, VBR	
	GOP Structure	GOP_0_B, GOP_1_B, GOP_2_B, GOP_3_B	
	Advanced Pretreatment	De-interlacing, noise reduction	
Audio	Encoding	MPEG-1 Layer 2, AC3 (2.0)	
	Sampling rate	48KHz	
	Resolution	24-bit	
	Bit-rate	64Kbps,128Kbps,192kbps,256kbps,320kbps,384kbps each channel	
Multiplexing	Maximum PID Remapping	180 input per channel	
	Function	PID remapping (automatically or manually) Accurate PCR adjusting	

		Generate PSI/ SI table automatically			
Scrambling (Only for DVB-C RF out)	Maximum simulcrypt CA	4			
	Standard	ETR289, ETSI 101 197, ETSI 103 197			
	Connection	Local/remote connection			
Modulation	DVB-C	RF out	4*RF DVB-C out (4 carriers combined output)		
		Standard	EN300 429/ITU-T J.83A/B		
		MER	≥40db		
		RF frequency	50~960MHz, 1KHz step		
		RF output level	-25 ~ -1 dBm, 1dBm step		
		Symbol Rate	5.0Msps~7.0Msps, 1ksps stepping		
		Constellation	16/32/64/128/256QAM		
		Constellation	J.83A	J.83B	
			16/32/64/128/256QAM	64/256 QAM	
	Bandwidth	8M	6M		
	DVB-T	RF out	4*RF DVB-T out (4 carriers combined output)		
		Standard	EN300744		
		FFT mode	2K,		
		Bandwidth	6M, 7M, 8M		
		Constellation	QPSK, 16QAM, 64QAM		
		Guard Interval	1/4, 1/8, 1/16, 1/32		
		FEC	1/2, 2/3, 3/4, 5/6, 7/8		
		MER	≥42 dB		
		RF frequency	50~960MHz, 1KHz step		
RF output level	-28~ -3 dBm, 1 dBm step				
Stream output	RF output (F type interface)				
	4 IP MPTS output over UDP/RTP, 1*1000M Base-T Ethernet interface				
System function	Network management (WEB)				
	English language				
	Ethernet software upgrade				
Miscellaneous	Dimension (W×L×H)	482mm×410mm×44mm			
	Environment	0~45°C(work); -20~80°C (Storage)			
	Power requirements	AC 110V± 10%, 50/60Hz, AC 220 ± 10%, 50/60Hz			

Order Information

	HPS824IV (DVB-C RF out)	HPS824IV (DVB-T RF out)	HPS824IA (DVB-C RF out)	HPS824IA (DVB-T RF out)
Scrambling Function	√		√	
DVB-C/T/T2/ISDBT Tuner Input	√		√	
OSD Insertion Function			√	√