HPS7112 DVB-S QPSK Modulator

Outline:

HPS7112 DVB-S QPSK modulator supports ASI input to receive data of DVB transport stream from encoder, multiplexer, DVB gateway, scrambler, video server and other equipments, and then after RS coding, convolution interleaving and QPSK modulation channel processing, it gives RF DVB-S/QPSK signal output. It suits 1U rack and can be configured by front panel LCD and NMS (network management software). Its high-integrated and cost-effective design makes it widely used in varieties of digital wireless broadcasting distribution systems.

Feature:

1. ASI input
2. RF DVB-S/QPSK output
3. 8M cache, anti-burst stream

4. Auto-filled streams filling and intelligent null packet removal

5. PCR correction

6. LCD/Keyboard control by front panel and network management by Ethernet

Specification:

Input Interface			ASI (DVB standard, BNC interface)
Output Interface			RF, F-head
Modulation Mode			DVB-S/QPSK
Impedance			75Ω
Output Frequency Range			950~2150MHz (1Khz step)
Symbol Rate			1~45Mbaud/s
Error-Correcting Codes			RS code188/204
MER			=38dB
Output Level			85~105dBV
Level Attenuation Range			-20dBm~0dBm (0.5db step)
Control			network RJ-45, 10/100M
			Keyboard+LCD
General Features		Size	482mm×280mm×44mm
		Temperature Range	0~45°C (Operation); -20~80°C (Storage)
		Power	100-240VAC, 50Hz, 25W